

FIG. 4A

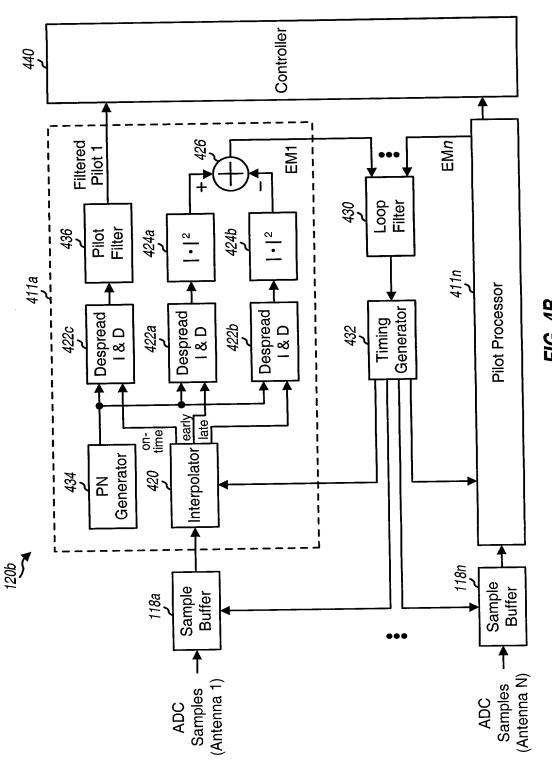
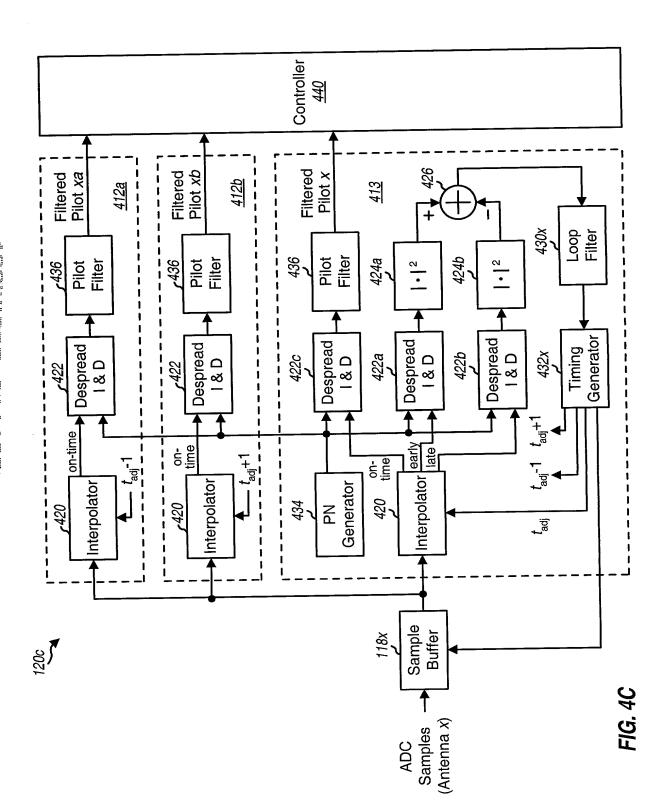
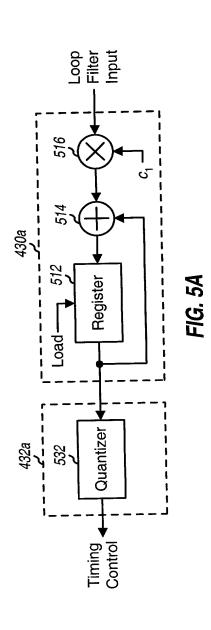


FIG. 4B





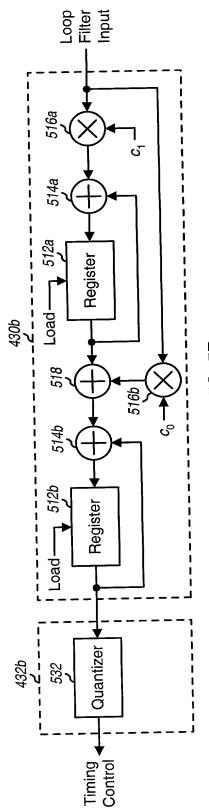
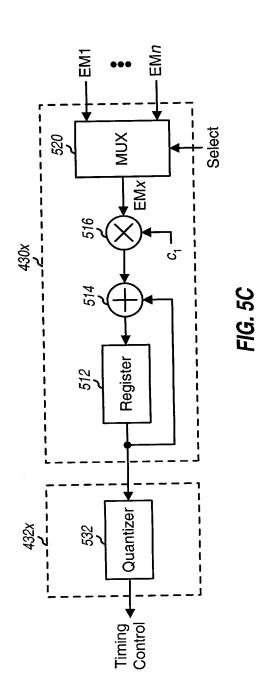


FIG. 5B



EMn EM1 EM! ٦ ٽ 514 Register 512 Quantizer 432y 532 Timing Control

FIG. 5D

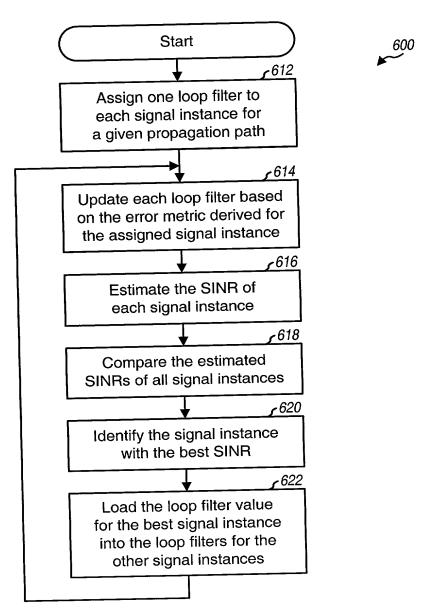


FIG. 6A

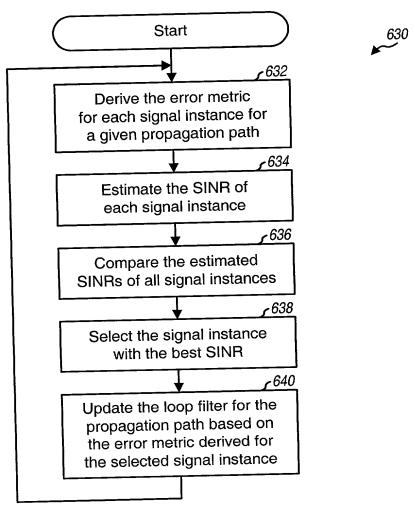


FIG. 6B

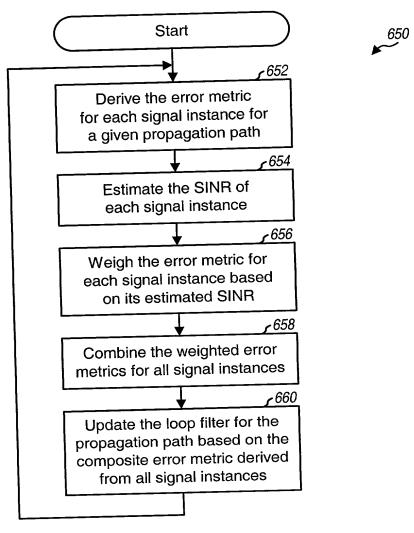


FIG. 6C

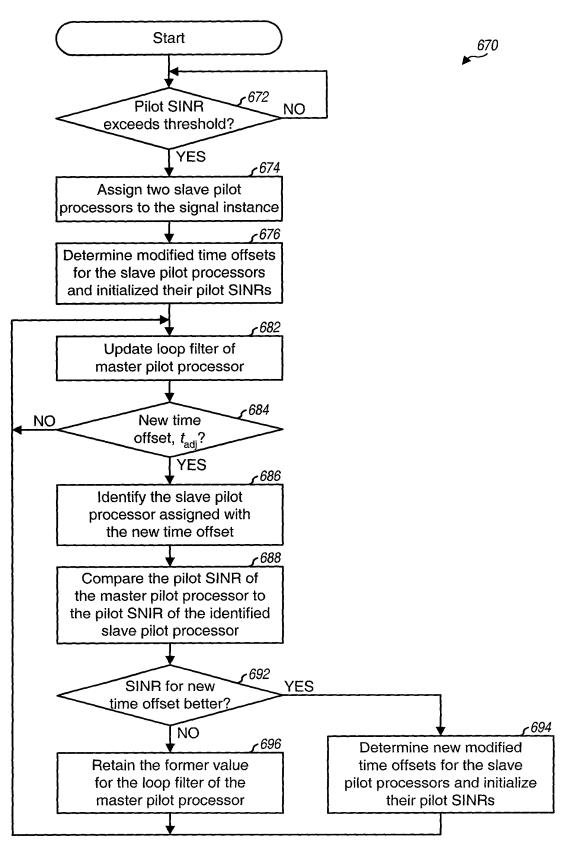


FIG. 6D